

WEST Search History

DATE: Friday, November 25, 2005

<u>Hide?</u>	<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>
	<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L150	(6931641 or 6658447 or 5327526).pn.	3
<input type="checkbox"/>	L149	542189.ap.	4
<input type="checkbox"/>	L148	L144 and L146	3
<input type="checkbox"/>	L147	L32 and L146	13165
<input type="checkbox"/>	L146	L89 or L100	72412
<input type="checkbox"/>	L145	L41 and L144	1
<input type="checkbox"/>	L144	L1 same L143	32
<input type="checkbox"/>	L143	branch\$3 with condition\$4	24124
<input type="checkbox"/>	L142	(L38 with branch\$3 with condition\$4) and L141	8
<input type="checkbox"/>	L141	L89 or router\$1	72412
<input type="checkbox"/>	L140	6829697.pn.	1
<input type="checkbox"/>	L139	L137 and L38	4
<input type="checkbox"/>	L138	connect\$3 with L137	41
<input type="checkbox"/>	L137	logic blocks with series	186
<input type="checkbox"/>	L136	L38 same L135	30
<input type="checkbox"/>	L135	single pipeline	758
<input type="checkbox"/>	L134	functional unit\$1 with L38 with branch\$3	7
<input type="checkbox"/>	L133	pipeline stage\$1 with L38 with branch\$3	1
<input type="checkbox"/>	L132	pipeline same conditional branch same packet\$1	20
<input type="checkbox"/>	L131	pipelined logic blocks	3
<input type="checkbox"/>	L130	pipelined logic blocksL129	0
<input type="checkbox"/>	L129	serial logic blocks	0
<input type="checkbox"/>	L128	series same L126	12
<input type="checkbox"/>	L127	L38 and L126	29
<input type="checkbox"/>	L126	serial same L123	143
<input type="checkbox"/>	L125	L38 with L123	8
<input type="checkbox"/>	L124	L114 with L123	2
<input type="checkbox"/>	L123	protocol processor\$1	1345
	<i>DB=USPT; PLUR=NO; OP=ADJ</i>		
<input type="checkbox"/>	L122	protocol processor\$1	766
<input type="checkbox"/>	L121	branch\$3 same L120	1

<input type="checkbox"/>	L120	L114 same packet\$1 same (switch\$3 or swap\$4)	100
<input type="checkbox"/>	L119	L114 and L52	13
<input type="checkbox"/>	L118	L114 and L117	0
<input type="checkbox"/>	L117	L65 with (serial or pipelin\$3)	18
<input type="checkbox"/>	L116	serial and L115	16
<input type="checkbox"/>	L115	L113 with L114	34
<input type="checkbox"/>	L114	multi-threading or multithreading or thread\$1	241890
<input type="checkbox"/>	L113	packet process\$3	3899
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L112	L6 with L38	182
<input type="checkbox"/>	L111	suspend\$3 same L110	5
<input type="checkbox"/>	L110	L6 same L38	243
<input type="checkbox"/>	L109	L6 and L38	867
<input type="checkbox"/>	L108	L100 same L106	3
<input type="checkbox"/>	L107	L100 and L106	123
<input type="checkbox"/>	L106	L38 with suspend\$3	2428
<input type="checkbox"/>	L105	L103 and L104	3
<input type="checkbox"/>	L104	L1 and L101	320
<input type="checkbox"/>	L103	packet\$1 and L102	47
<input type="checkbox"/>	L102	L38.ti,ab,clm. and L100.ti,ab,clm.	144
<input type="checkbox"/>	L101	L38 and L100	5785
<input type="checkbox"/>	L100	router\$1	59140
<input type="checkbox"/>	L99	L89 and L98	6
<input type="checkbox"/>	L98	(712/23).ccls.	704
<input type="checkbox"/>	L97	L65 and L94	1
<input type="checkbox"/>	L96	L49 and L94	1
<input type="checkbox"/>	L95	L89 and L94	4
<input type="checkbox"/>	L94	(712/241).ccls.	254
<input type="checkbox"/>	L93	L70 and L89	0
<input type="checkbox"/>	L92	L70 and L91	0
<input type="checkbox"/>	L91	L38 and L89	1280
<input type="checkbox"/>	L90	L65 and L89	24
<input type="checkbox"/>	L89	packet switch\$3	21050
<input type="checkbox"/>	L88	sun.as. and treat.xp. and L38	9
<input type="checkbox"/>	L87	L38 same L86	37
<input type="checkbox"/>	L86	instruction block\$1	2021
<input type="checkbox"/>	L85	L38 and L83	5
<input type="checkbox"/>	L84	packet\$1 and L83	13

<input type="checkbox"/>	L83	decoupled processor\$1	61
<input type="checkbox"/>	L82	L81 adj processor\$1	5
<input type="checkbox"/>	L81	non-blocking or nonblocking	6923
<input type="checkbox"/>	L80	mon-blocking or nonblocking	1286
<input type="checkbox"/>	L79	L38 with L65	20
<input type="checkbox"/>	L78	L38 adj processor	754
<input type="checkbox"/>	L77	L65 and L70	1
<input type="checkbox"/>	L76	L62 and L70	3
<input type="checkbox"/>	L75	L70 and L74	1
<input type="checkbox"/>	L74	(712/32).ccls.	373
<input type="checkbox"/>	L73	context switch\$3 same L59	33
<input type="checkbox"/>	L72	L59 and L70	1
<input type="checkbox"/>	L71	L49 and L70	0
<input type="checkbox"/>	L70	(712/235).ccls.	188
<input type="checkbox"/>	L69	packet\$1.ti,ab,clm. and L68	9
<input type="checkbox"/>	L68	L65 with (parallel or pipelin\$3)	146
<input type="checkbox"/>	L67	packet\$1.ti,ab,clm. and L65	81
<input type="checkbox"/>	L66	packet.ti,ab,clm. and L65	67
<input type="checkbox"/>	L65	subprocessor\$1 or sub-processor\$1	1185
<input type="checkbox"/>	L64	L38 same L62	30
<input type="checkbox"/>	L63	packet\$1 same L62	18
<input type="checkbox"/>	L62	non-blocking with processor\$1	348
<input type="checkbox"/>	L61	L60 not L58	5
<input type="checkbox"/>	L60	L59 and L53	14
<input type="checkbox"/>	L59	packet with processor\$1	13263
<input type="checkbox"/>	L58	L49 and L53	9
<input type="checkbox"/>	L57	packet\$1 and L52	22
<input type="checkbox"/>	L56	L52 and L55	8
<input type="checkbox"/>	L55	conditional branch\$3	5445
<input type="checkbox"/>	L54	L52 and L53	0
<input type="checkbox"/>	L53	(712/234).ccls.	352
<input type="checkbox"/>	L52	serial processors	99
<input type="checkbox"/>	L51	L38 with L49	30
<input type="checkbox"/>	L50	L38 and L49	199
<input type="checkbox"/>	L49	packet processor\$1	1652
<input type="checkbox"/>	L48	L47 not private branch exchange	36
<input type="checkbox"/>	L47	L45 not L46	48

<input type="checkbox"/>	L46	branch\$3 same L44	5
<input type="checkbox"/>	L45	branch\$3 and L44	53
<input type="checkbox"/>	L44	L38 same L41	253
<input type="checkbox"/>	L43	branch\$3 and L42	521
<input type="checkbox"/>	L42	L38 and L41	2963
<input type="checkbox"/>	L41	decoupled	29350
<input type="checkbox"/>	L40	L38 and L39	8
<input type="checkbox"/>	L39	decoupled architecture	34
<input type="checkbox"/>	L38	multi-threading or multithreading or thread\$1	305828
<input type="checkbox"/>	L37	chained processor\$1	8
<input type="checkbox"/>	L36	L28 and L35	0
<input type="checkbox"/>	L35	L34 with buffer\$1	0
<input type="checkbox"/>	L34	reconfigurable pipeline\$1	100
<input type="checkbox"/>	L33	L29 and L32	2
<input type="checkbox"/>	L32	709/\$.ccls.	40442
<input type="checkbox"/>	L31	router\$1 and L29	19
<input type="checkbox"/>	L30	L28 and L29	2
<input type="checkbox"/>	L29	reservation station\$1	1048
<input type="checkbox"/>	L28	internet protocol or tcp	73404
<input type="checkbox"/>	L27	L25 and L26	18
<input type="checkbox"/>	L26	reservation station\$1 with branch\$3	329
<input type="checkbox"/>	L25	internet protocol or ip or tcp	136611
<input type="checkbox"/>	L24	reservation station\$1 and L23	10
<input type="checkbox"/>	L23	(data or network) adj packet\$1	50716
<input type="checkbox"/>	L22	(router\$1 or switch\$2) and L21	83
<input type="checkbox"/>	L21	packet\$1 and reservation station\$1	156
<input type="checkbox"/>	L20	L18 same next instruction	18
<input type="checkbox"/>	L19	pipeline stage\$1 with conditional branch\$3	111
<input type="checkbox"/>	L18	pipeline stage\$1 with branch\$3	526
<input type="checkbox"/>	L17	L16 not L10	64
<input type="checkbox"/>	L16	stall\$3 and L15	65
<input type="checkbox"/>	L15	L6 same thread\$1	235
<input type="checkbox"/>	L14	L11 and L6	1
<input type="checkbox"/>	L13	serial sub-processors	0
<input type="checkbox"/>	L12	serial subprocessors	0
<input type="checkbox"/>	L11	serial processors	99
<input type="checkbox"/>	L10	L6 and L9	14

<input type="checkbox"/>	L9	stall\$3 same L1	89
<input type="checkbox"/>	L8	stall\$3 and L7	110
<input type="checkbox"/>	L7	L1 and L6	250
<input type="checkbox"/>	L6	packet process\$3	8317
<input type="checkbox"/>	L5	conditional branch\$3 same L4	6
<input type="checkbox"/>	L4	new thread	1711
<input type="checkbox"/>	L3	conditional branch\$3 same L1	20
<input type="checkbox"/>	L2	conditional branch\$3 with L1	2
<input type="checkbox"/>	L1	context adj (switch\$3 or swap\$4)	4274

END OF SEARCH HISTORY